

Notice of References Cited	Application/Control No. 09/965,896	Applicant(s)/Patent Under Reexamination NAGASE ET AL.	
	Examiner Stephen M. Baker	Art Unit 2133	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,182,261	01-2001	Haller et al.	714/758
	B	US-6,233,709	05-2001	Zhang et al.	714/774
	C	US-6,292,918	09-2001	Sindhushayana et al.	714/755
	D	US-6,308,294	10-2001	Ghosh et al.	714/751
	E	US-6,510,536	01-2003	Crozier et al.	714/755
	F	US-6,526,531	02-2003	Wang, Xiao-An	714/704
	G	US-6,557,139	04-2003	Bohnke, Ralf	714/758
	H	US-6,591,390	07-2003	Yagyu, Mitsuhiro	714/755
	I	US-6,615,385	09-2003	Kim et al.	714/758
	J	US-6,665,357	12-2003	Somayazulu, V. Srinivasa	375/341
	K	US-6,675,342	01-2004	Yagyu, Mitsuhiro	714/755
	L	US-6,678,857	01-2004	Sindhushayana et al.	714/755
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hong et al., "VLSI Circuit Complexity and Decoding Performance Analysis for Low-Power RSC Turbo-Code and Iterative Block Decoders Design", MILCOM 98, October 1998, pp. 708-712.
	V	Zhai et al., "New Error Detection Techniques and Stopping Criteria for Turbo Decoding", 2000 Canadian Conference on Electrical and Computer Engineering, March 2000, pp. 58-62.
	W	Wu et al., "A Simple Stopping Criterion for Turbo Decoding", IEEE Communications Letters, Vol. 4, No. 8, August 2000, pp. 258-260.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.